

IN THE CLAIMS

Presented below are all of the pending claims in a clean, unmarked format. Claims 2 and 16 have been cancelled. New claims 19 through 22 have been added. Claims that have not been amended are included with the notation "Unamended".

65 *See B2* 1 (Once amended) A method, comprising:

2 providing a first resistor with a first end and a second end, said
3 first end coupled to a switch and said second end coupled to
4 a data bus wire at a near end of a data bus;
5 controlling said switch with a detach control signal sent from a far
6 end of said data bus; and
7 switching a biasing voltage from said resistor utilizing said switch.

1 3. (Unamended) The method of claim 1, wherein said first
2 resistor is configured as a pull-up resistor.

1 4. (Unamended) The method of claim 3, further comprising
2 detecting said switching of said biasing voltage.

1 5. (Unamended) The method of claim 4, further comprising
2 determining a logically detached state responsive to said detecting.

1 6. (Unamended) The method of claim 1, wherein said detach
2 control signal is responsive to a wake-up signal.

1 7. (Unamended) The method of claim 6, wherein said detach
2 control signal is asserted when said wake-up signal is de-asserted.

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1 8. (Once amended) An apparatus, comprising:
2 a first resistor with a first end and a second end;
3 a switch coupled to said first end of said first resistor and to a bias
4 voltage;
5 a detach control signal wire of a data bus coupled to said switch at
6 a near end of said data bus, to receive a detach control
7 signal sent from a far end of said data bus; and
8 a data bus wire of said data bus coupled to said second end of said
9 first resistor.

1 9. (Once amended) The apparatus of claim 8, wherein said
2 switch may apply said bias voltage to said first end of said first resistor
3 responsively to said detach control signal on said detach control signal
4 wire.

1 10. (Unamended) The apparatus of claim 9, wherein said detach
2 control signal is generated responsively to a wake-up signal.

1 11. (Once amended) The apparatus of claim 8, wherein said
2 data bus carries universal serial bus data.

1 12. (Once amended) The apparatus of claim 8, wherein said
2 data bus carries IEEE-1394 bus data.

13. (Unamended) The apparatus of claim 8, further comprising
a second resistor with a first end and a second end, said first end
coupled to said data bus wire.

14. (Once amended) The apparatus of claim 13, wherein said
second end of said second resistor is coupled to signal ground.

15. (Once amended) An apparatus comprising:
means for providing a first resistor with a first end and a second
end, said first end coupled to a switch and said second end
coupled to a data bus wire at a near end of a data bus;
means for controlling said switch with a detach control signal sent
from a far end of said data bus; and
means for switching a biasing voltage from said resistor utilizing
said switch.

17. (Once amended) The apparatus of claim 15, further
comprising
means for detecting said switching of said biasing voltage.

18. (Once amended) The apparatus of claim 15, wherein said
detach control signal is responsive to a wake-up signal.

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2* 19. (New) A system, comprising:

3 a data bus with a near end and a far end;

4 a first circuit, coupled to said near end, including a first resistor

5 with a first end and a second end, a switch coupled to said first end of

6 said first resistor and to a bias voltage, a data bus wire of said data bus

7 coupled to said second end of said first resistor, a detach control signal

8 wire of said data bus coupled to said switch to receive a detach control

9 signal; and

10 a second circuit, coupled to said far end, to send said detach

control signal.

1 20. (New) The system of claim 19, wherein said switch may

2 apply said bias voltage to said first end of said first resistor responsively

3 to said detach control signal.

1 21. (New) The system of claim 20, wherein said detach control

2 signal is sent in response to a wake-up signal.

1 22. (New) The system of claim 21, wherein said wake-up signal

2 is sent by said first circuit.